

IN THE CLAIMS:

This listing of claims will replace all prior versions, and listings, of claims in the application:

1-45. (Canceled)

46. (Currently Amended) A semiconductor device comprising:

a first thin film transistor formed over an insulating surface, the first thin film transistor comprising:

a semiconductor film comprising crystalline silicon and having at least source and drain regions and a channel forming region;

a gate insulating film over the channel forming region; and

a gate electrode formed over the gate insulating film;

an interlayer insulating film formed over the first thin film transistor;

a first conductive layer formed over the interlayer insulating film and electrically connected to one of the source and drain regions of the first thin film transistor;

a color filter formed over the interlayer insulating film and the first conductive layer, wherein the color filter covers the entire first thin film transistor; and

a pixel electrode formed over the color filter and electrically connected to the first conductive layer.

47. (Currently Amended) A semiconductor device comprising:

a first thin film transistor formed over an insulating surface, the first thin film transistor comprising:

a semiconductor film comprising at least a channel forming region;

a gate insulating film adjacent to the channel forming region; and

a gate electrode adjacent to the gate insulating film,

an interlayer insulating film formed over the first thin film transistor;

a first conductive layer formed over the interlayer insulating film and electrically connected to one of source and drain regions of the first thin film transistor;

a color filter formed over the interlayer insulating film and the first conductive layer, wherein the color filter covers the entire first thin film transistor; and

a pixel electrode formed over the color filter and electrically connected to the first conductive layer.

48. (Previously presented) A semiconductor device comprising:

a first thin film transistor formed over an insulating surface, the first thin film transistor comprising:

a semiconductor film comprising crystalline silicon and having at least source and drain regions and a channel forming region;

a gate insulating film adjacent to the channel forming region; and

a gate electrode adjacent to the channel forming region with the gate insulating film interposed therebetween;

an interlayer insulating film formed over the first thin film transistor, the interlayer insulating film comprising at least a material selected from the group consisting of silicon nitride, silicon oxide and nitrated silicon oxide;

a color filter formed over the interlayer insulating film, wherein the color filter covers the entire first thin film transistor; and

a pixel electrode formed over the color filter,

wherein the pixel electrode is electrically connected to the first thin film transistor.

49. (Previously presented) A device according to claim 48, wherein the gate electrode is located over the channel forming region.

50-51. (Canceled).

52. (Currently Amended) A semiconductor device comprising:

a first thin film transistor formed over an insulating surface, the first thin film transistor comprising:

a semiconductor film comprising crystalline silicon and having at least source and drain regions and a channel forming region;

a gate insulating film adjacent to the channel forming region; and

a gate electrode formed adjacent to the channel forming region with the gate insulating film interposed therebetween;

a first interlayer insulating film formed over the first thin film transistor;

a first conductive layer formed over the first interlayer insulating film and electrically connected to one of the source and drain regions of the first thin film transistor;

a passivation film formed over the first conductive layer, the passivation film comprising at least a material selected from the group consisting of silicon nitride, silicon oxide and nitrated silicon oxide;

a color filter formed over the passivation film, wherein the color filter covers the entire first thin film transistor; and

a pixel electrode formed over the color filter and electrically connected to the first conductive layer.

53. (Previously presented) A device according to claim 52, wherein the gate electrode is located over the channel forming region.

54-55. (Canceled).

56. (Currently Amended) A semiconductor device comprising:

a first thin film transistor formed over an insulating surface, the first thin film transistor comprising:

a semiconductor film comprising :

a channel forming region; and

a source region and a drain region;

a gate insulating film over the channel forming region; and

a gate electrode over the gate insulating film;

an interlayer insulating film formed over the first thin film transistor;

a first conductive layer formed over the interlayer insulating film and electrically connected to one of source and drain regions of the first thin film transistor;

a color filter formed over the interlayer insulating film, the first conductive layer and the first thin film transistor, wherein the color filter covers the entire first thin film transistor; and

a pixel electrode formed over the color filter and electrically connected to the first conductive layer.

57. (Previously presented) A semiconductor device comprising:

a first thin film transistor formed over an insulating surface, the first thin film transistor comprising:

 a semiconductor film comprising:

 a channel forming region; and

 a source region and a drain region;

 a gate insulating film adjacent to the channel forming region; and

 a gate electrode adjacent to the channel forming region with the gate insulating film interposed therebetween;

 an interlayer insulating film formed over the first thin film transistor, the interlayer insulating film comprising at least a material selected from the group consisting of silicon nitride, silicon oxide and nitrated silicon oxide;

 a color filter formed over the interlayer insulating film and the first thin film transistor, wherein the color filter covers the entire first thin film transistor; and

 a pixel electrode formed over the color filter.

58. (Currently Amended) A semiconductor device comprising:

a first thin film transistor formed over an insulating surface, the first thin film transistor comprising:

 a semiconductor film comprising:

 a channel forming region; and

 a source region and a drain region;

 a gate insulating film adjacent to the channel forming region; and

a gate electrode adjacent to the channel forming region with the gate insulating film interposed therebetween;

a first interlayer insulating film formed over the first thin film transistor;

a first conductive layer formed over the first interlayer insulating film and electrically connected to one of the source and drain regions of the first thin film transistor;

a passivation film formed over the first conductive layer, the passivation film comprising at least a material selected from the group consisting of silicon nitride and nitrated silicon oxide;

a color filter formed over the passivation film and the first thin film transistor, wherein the color filter covers the entire first thin film transistor; and

a pixel electrode formed over the color filter and electrically connected to the first conductive layer.

59. (Currently Amended) A semiconductor device comprising:

a first thin film transistor comprising:

a semiconductor film comprising at least a channel forming region;

a gate insulating film over the channel forming region; and

a gate electrode over the channel forming region with the gate insulating film interposed therebetween;

an interlayer insulating film formed over the first thin film transistor;

a first conductive layer formed over the interlayer insulating film and electrically connected to one of source and drain regions of the first thin film transistor;

a color filter formed over the interlayer insulating film, the first conductive layer and the first thin film transistor, wherein the color filter covers the entire first thin film transistor; and

a pixel electrode formed over the color filter and electrically connected to the first conductive layer.

60. (Previously presented) A semiconductor device comprising:

a first thin film transistor comprising:

a semiconductor film comprising silicon and having at least a channel forming region;

a gate insulating film adjacent to the channel forming region; and

a gate electrode adjacent to the channel forming region with the gate insulating film interposed therebetween;

an interlayer insulating film formed over the first thin film transistor, the interlayer insulating film comprising at least a material selected from the group consisting of silicon nitride, silicon oxide and nitrated silicon oxide;

a color filter formed over the interlayer insulating film and the first thin film transistor, wherein the color filter covers the entire first thin film transistor; and

a pixel electrode formed over the color filter.

61. (Currently Amended) A semiconductor device comprising:

a first thin film transistor comprising:

a semiconductor film comprising silicon and having at least a channel forming region;

a gate insulating film adjacent to the channel forming region; and

a gate electrode adjacent to the channel forming region with the gate insulating film interposed therebetween;

a first interlayer insulating film formed over the first thin film transistor;

a first conductive layer formed over the first interlayer insulating film and electrically connected to one of the source and drain regions of the first thin film transistor;

a passivation film formed over the first conductive layer, the passivation film comprising at least a material selected from the group consisting of silicon nitride and nitrated silicon oxide;

a color filter formed over the passivation film and the first thin film transistor, wherein the color filter covers the entire first thin film transistor; and

a pixel electrode formed over the color filter and electrically connected to the first conductive layer.

62. (Previously presented) A device according to claim 56, wherein the semiconductor film comprises crystalline silicon.

63. (Previously presented) A device according to claim 57, wherein the semiconductor film comprises crystalline silicon.

64. (Previously presented) A device according to claim 58, wherein the semiconductor film comprises crystalline silicon.

65. (Previously presented) A device according to claim 59, wherein the semiconductor film comprises crystalline silicon.

66. (Previously presented) A device according to claim 60, wherein the semiconductor film comprises crystalline silicon.

67. (Previously presented) A device according to claim 61, wherein the semiconductor film comprises crystalline silicon.

68. (Previously presented) A device according to claim 46, wherein the semiconductor device further comprising:

a resin film over the color filter;

an electrode over the organic resin film; and

an oxide film of the electrode in direct contact with at least a portion of a surface of the electrode,

wherein the pixel electrode is in direct contact with at least a portion of the oxide film, and

wherein a storage capacitor comprises the electrode and the pixel electrode with the oxide film interposed therebetween.

69. (Previously presented) A device according to claim 48, wherein the semiconductor device further comprising:

a resin film over the color filter;

an electrode over the organic resin film; and

an oxide film of the electrode in direct contact with at least a portion of a surface of the electrode,

wherein the pixel electrode is in direct contact with at least a portion of the oxide film,

and

wherein a storage capacitor comprises the electrode and the pixel electrode with the oxide film interposed therebetween.

70. (Previously presented) A device according to claim 52, wherein the semiconductor device further comprising:

a resin film over the color filter;

an electrode over the organic resin film; and

an oxide film of the electrode in direct contact with at least a portion of a surface of the electrode,

wherein the pixel electrode is in direct contact with at least a portion of the oxide film, and

wherein a storage capacitor comprises the electrode and the pixel electrode with the oxide film interposed therebetween.

71. (Previously presented) A device according to claim 46, wherein the semiconductor film further comprises LDD regions between the channel forming region and the source and drain regions.

72. (Previously presented) A device according to claim 48, wherein the semiconductor film further comprises LDD regions between the channel forming region and the source and drain regions.

73. (Previously presented) A device according to claim 52, wherein the semiconductor film further comprises LDD regions between the channel forming region and the source and drain regions.

74. (Previously presented) A device according to claim 56, wherein the semiconductor film further comprises LDD regions between the channel forming region and the source and drain regions.

75. (Previously presented) A device according to claim 57, wherein the semiconductor film further comprises LDD regions between the channel forming region and the source and drain regions.

76. (Previously presented) A device according to claim 58, wherein the semiconductor film further comprises LDD regions between the channel forming region and the source and drain regions.

77. (Previously presented) A device according to claim 46, further comprising a driver circuit comprising a second thin film transistor,

wherein the first thin film transistor is included in a pixel matrix circuit, and
wherein the pixel matrix circuit and the driver circuit are formed over an insulating surface.

78. (Previously presented) A device according to claim 47, further comprising a driver circuit comprising a second thin film transistor,

wherein the first thin film transistor is included in a pixel matrix circuit, and
wherein the pixel matrix circuit and the driver circuit are formed over an insulating
surface.

79. (Previously presented) A device according to claim 48, further comprising a driver
circuit comprising a second thin film transistor,

wherein the first thin film transistor is included in a pixel matrix circuit, and
wherein the pixel matrix circuit and the driver circuit are formed over an insulating
surface.

80. (Canceled).

81. (Previously presented) A device according to claim 52, further comprising a driver
circuit comprising a second thin film transistor,

wherein the first thin film transistor is included in a pixel matrix circuit, and
wherein the pixel matrix circuit and the driver circuit are formed over an insulating
surface.

82. (Canceled).

83. (Previously presented) A device according to claim 56, further comprising a driver
circuit comprising a second thin film transistor,

wherein the first thin film transistor is included in a pixel matrix circuit, and
wherein the pixel matrix circuit and the driver circuit are formed over an insulating

surface.

84. (Previously presented) A device according to claim 57, further comprising a driver circuit comprising a second thin film transistor,

wherein the first thin film transistor is included in a pixel matrix circuit, and

wherein the pixel matrix circuit and the driver circuit are formed over an insulating surface.

85. (Previously presented) A device according to claim 58, further comprising a driver circuit comprising a second thin film transistor,

wherein the first thin film transistor is included in a pixel matrix circuit, and

wherein the pixel matrix circuit and the driver circuit are formed over an insulating surface.

86. (Previously presented) A device according to claim 59, further comprising a driver circuit comprising a second thin film transistor,

wherein the first thin film transistor is included in a pixel matrix circuit, and

wherein the pixel matrix circuit and the driver circuit are formed over an insulating surface.

87. (Previously presented) A device according to claim 60, further comprising a driver circuit comprising a second thin film transistor,

wherein the first thin film transistor is included in a pixel matrix circuit, and

wherein the pixel matrix circuit and the driver circuit are formed over an insulating

surface.

88. (Previously presented) A device according to claim 61, further comprising a driver circuit comprising a second thin film transistor,

wherein the first thin film transistor is included in a pixel matrix circuit, and

wherein the pixel matrix circuit and the driver circuit are formed over an insulating surface.

89. (Previously presented) A device according to claim 46, wherein the semiconductor device is selected from the group consisting of a personal computer, a video camera, a mobile computer, a goggle type display, a player that uses a recording medium, a camera, a projector, a portable telephone, a portable book and a display device.

90. (Previously presented) A device according to claim 47, wherein the semiconductor device is selected from the group consisting of a personal computer, a video camera, a mobile computer, a goggle type display, a player that uses a recording medium, a camera, a projector, a portable telephone, a portable book and a display device.

91. (Previously presented) A device according to claim 48, wherein the semiconductor device is selected from the group consisting of a personal computer, a video camera, a mobile computer, a goggle type display, a player that uses a recording medium, a camera, a projector, a portable telephone, a portable book and a display device.

92. (Previously presented) A device according to claim 52, wherein the semiconductor

device is selected from the group consisting of a personal computer, a video camera, a mobile computer, a goggle type display, a player that uses a recording medium, a camera, a projector, a portable telephone, a portable book and a display device.

93. (Previously presented) A device according to claim 56, wherein the semiconductor device is selected from the group consisting of a personal computer, a video camera, a mobile computer, a goggle type display, a player that uses a recording medium, a camera, a projector, a portable telephone, a portable book and a display device.

94. (Previously presented) A device according to claim 57, wherein the semiconductor device is selected from the group consisting of a personal computer, a video camera, a mobile computer, a goggle type display, a player that uses a recording medium, a camera, a projector, a portable telephone, a portable book and a display device.

95. (Previously presented) A device according to claim 58, wherein the semiconductor device is selected from the group consisting of a personal computer, a video camera, a mobile computer, a goggle type display, a player that uses a recording medium, a camera, a projector, a portable telephone, a portable book and a display device.

96. (Previously presented) A device according to claim 59, wherein the semiconductor device is selected from the group consisting of a personal computer, a video camera, a mobile computer, a goggle type display, a player that uses a recording medium, a camera, a projector, a portable telephone, a portable book and a display device.

97. (Previously presented) A device according to claim 60, wherein the semiconductor device is selected from the group consisting of a personal computer, a video camera, a mobile computer, a goggle type display, a player that uses a recording medium, a camera, a projector, a portable telephone, a portable book and a display device.

98. (Previously presented) A device according to claim 61, wherein the semiconductor device is selected from the group consisting of a personal computer, a video camera, a mobile computer, a goggle type display, a player that uses a recording medium, a camera, a projector, a portable telephone, a portable book and a display device.

99. (Previously presented) A device according to claim 46 wherein said color filter has a flat upper surface.

100. (Previously presented) A device according to claim 47 wherein said color filter has a flat upper surface.

101. (Previously presented) A device according to claim 48 wherein said color filter has a flat upper surface.

102. (Previously presented) A device according to claim 52 wherein said color filter has a flat upper surface.

103. (Previously presented) A device according to claim 56 wherein said color filter has a flat upper surface.

104. (Previously presented) A device according to claim 57 wherein said color filter has a flat upper surface.

105. (Previously presented) A device according to claim 58 wherein said color filter has a flat upper surface.

106. (Previously presented) A device according to claim 59 wherein said color filter has a flat upper surface.

107. (Previously presented) A device according to claim 60 wherein said color filter has a flat upper surface.

108. (Previously presented) A device according to claim 61 wherein said color filter has a flat upper surface.

109. (Currently Amended) A device according to claim 46 wherein said color filter has an opening through which said pixel electrode is electrically connected to the first conductive layer.

110. (Currently Amended) A device according to claim 47 wherein said color filter has an opening through which said pixel electrode is electrically connected to the first conductive layer.

111. (Previously presented) A device according to claim 48 wherein said color filter has an opening through which said pixel electrode is electrically connected to the first thin film transistor.

112. (Currently Amended) A device according to claim 52 wherein said color filter has an opening through which said pixel electrode is electrically connected to the first conductive layer.

113. (Currently Amended) A device according to claim 56 wherein said color filter has an opening through which said pixel electrode is electrically connected to the first conductive layer.

114. (Previously presented) A device according to claim 57 wherein said color filter has an opening through which said pixel electrode is electrically connected to the first thin film transistor.

115. (Currently Amended) A device according to claim 58 wherein said color filter has an opening through which said pixel electrode is electrically connected to the first conductive layer.

116. (Currently Amended) A device according to claim 59 wherein said color filter has an opening through which said pixel electrode is electrically connected to the first conductive layer.

117. (Previously presented) A device according to claim 60 wherein said color filter has an opening through which said pixel electrode is electrically connected to the thin film transistor.

118. (Currently Amended) A device according to claim 61 wherein said color filter has an

opening through which said pixel electrode is electrically connected to the first conductive layer.

119. (Previously presented) A device according to claim 46, further comprising one or more gate electrodes in addition to the gate electrode.

120. (Previously presented) A device according to claim 47, further comprising one or more gate electrodes in addition to the gate electrode.

121. (Previously presented) A device according to claim 48, further comprising one or more gate electrodes in addition to the gate electrode.

122. (Previously presented) A device according to claim 52, further comprising one or more gate electrodes in addition to the gate electrode.

123. (Previously presented) A device according to claim 56, further comprising one or more gate electrodes in addition to the gate electrode.

124. (Previously presented) A device according to claim 57, further comprising one or more gate electrodes in addition to the gate electrode.

125. (Previously presented) A device according to claim 58, further comprising one or more gate electrodes in addition to the gate electrode.

126. (Previously presented) A device according to claim 59, further comprising one or

more gate electrodes in addition to the gate electrode.

127. (Previously presented) A device according to claim 60, further comprising one or more gate electrodes in addition to the gate electrode.

128. (Previously presented) A device according to claim 61, further comprising one or more gate electrodes in addition to the gate electrode.

129. (Previously presented) A device according to claim 46, wherein the gate electrode is covered by the interlayer insulating film.

130. (Previously presented) A device according to claim 47, wherein the gate electrode is covered by the interlayer insulating film.

131. (Previously presented) A device according to claim 48, wherein the gate electrode is covered by the interlayer insulating film.

132. (Previously presented) A device according to claim 52, wherein the gate electrode is covered by the interlayer insulating film.

133. (Previously presented) A device according to claim 56, wherein the gate electrode is covered by the interlayer insulating film.

134. (Previously presented) A device according to claim 57, wherein the gate electrode is

covered by the interlayer insulating film.

135. (Previously presented) A device according to claim 58, wherein the gate electrode is covered by the interlayer insulating film.

136. (Previously presented) A device according to claim 59, wherein the gate electrode is covered by the interlayer insulating film.

137. (Previously presented) A device according to claim 60, wherein the gate electrode is covered by the interlayer insulating film.

138. (Previously presented) A device according to claim 61, wherein the gate electrode is covered by the interlayer insulating film.

139. (New) A device according to claim 109, further comprising a second conductive layer formed over the interlayer insulating film and electrically connected to the other of the source and drain regions of the first thin film transistor,

wherein the color filter covers an entire surface of the first conductive layer and an entire surface of the second conductive layer except for a part of the first conductive layer overlapping the opening through which the pixel electrode is electrically connected to the first conductive layer.

140. (New) A device according to claim 110, further comprising a second conductive layer formed over the interlayer insulating film and electrically connected to the other of the source and drain regions of the first thin film transistor,

wherein the color filter covers an entire surface of the first conductive layer and an entire surface of the second conductive layer except for a part of the first conductive layer overlapping the opening through which the pixel electrode is electrically connected to the first conductive layer.

141. (New) A device according to claim 112, further comprising a second conductive layer formed over the interlayer insulating film and electrically connected to the other of the source and drain regions of the first thin film transistor,

wherein the color filter covers an entire surface of the first conductive layer and the second conductive layer except for a part of the first conductive layer overlapping the opening through which the pixel electrode is electrically connected to the first conductive layer.

142. (New) A device according to claim 113, further comprising a second conductive layer formed over the interlayer insulating film and electrically connected to the other of the source and drain regions of the first thin film transistor,

wherein the color filter covers an entire surface of the first conductive layer and an entire surface of the second conductive layer except for a part of the first conductive layer overlapping the opening through which the pixel electrode is electrically connected to the first conductive layer.

143. (New) A device according to claim 115, further comprising a second conductive layer formed over the interlayer insulating film and electrically connected to the other of the source and drain regions of the first thin film transistor,

wherein the color filter covers an entire surface of the first conductive layer and an entire surface of the second conductive layer except for a part of the first conductive layer overlapping the opening through which the pixel electrode is electrically connected to the first conductive layer.

144. (New) A device according to claim 116, further comprising a second conductive layer formed over the interlayer insulating film and electrically connected to the other of the source and drain regions of the first thin film transistor,

wherein the color filter covers an entire surface of the first conductive layer and an entire surface of the second conductive layer except for a part of the first conductive layer overlapping the opening through which the pixel electrode is electrically connected to the first conductive layer.

145. (New) A device according to claim 118, further comprising a second conductive layer formed over the interlayer insulating film and electrically connected to the other of the source and drain regions of the first thin film transistor,

wherein the color filter covers an entire surface of the first conductive layer and an entire surface of the second conductive layer except for a part of the first conductive layer overlapping the opening through which the pixel electrode is electrically connected to the first conductive layer.